UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No. END919970013US2

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TO THE ASSISTANT COMMISSIONER FOR PATENTS

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Washington, D.C. 20231 Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application LOW TEMPERATURE SOLDER CHIP ATTACH STRUCTURE.	n for ann
LOW TEMPERATURE SOLDER CHIP ATTACH STRUCTURE	. 101 2112
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and invented by:	
Joseph M. Milewski and Charles G. Woychik	
and Charles G. Woychik	
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Total Pages in this Submission 36

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4	6 .		Con	mputer Program in Mi	crofiche (Appendix)					
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Application Elements (Continued)

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No. END919970013US2

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Accompanying Application Parts (Continued)

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I hereby certify that the following correspondence: Divisional Application and related papers			Examiner	Group Art Unit
Invisional Application and related papers (Identify type of correspondence) is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" states and the United States Postal Service "Express Mail Post Office to Addressee" states and the Invited States Postal Service "Express Mail Post Office to Addressee" states and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States are states and Invited States and Invited States and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States are states and Invited States and Invited States and Invited States are states and Invited States and Invited States and In	ention: LOW TEMPERA	TURE SOLDER CHIP ATT	ACH STRUCTURE	
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